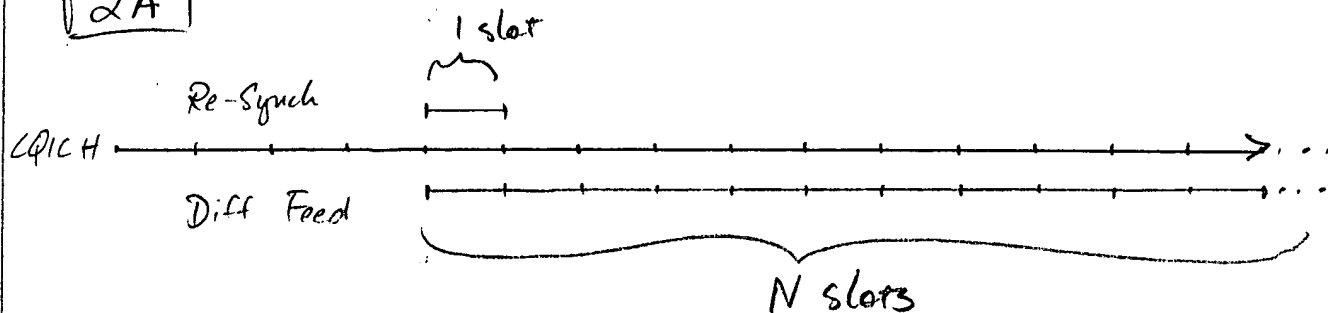


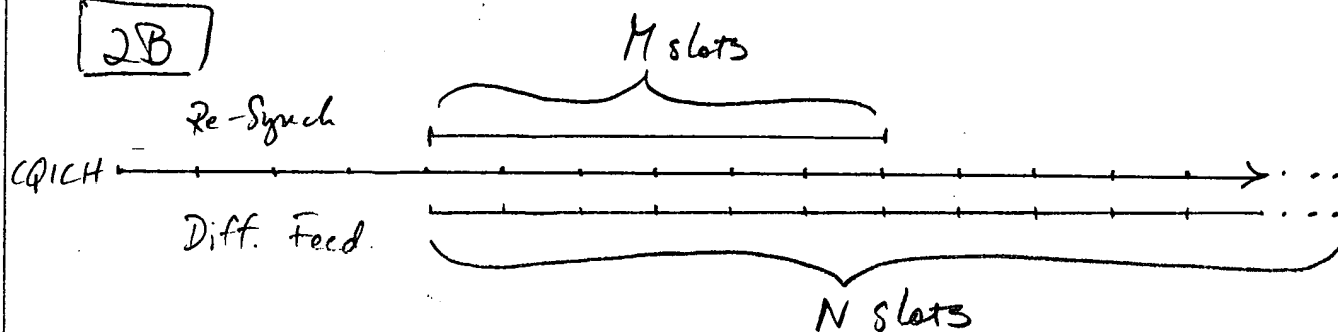
FIG. 1



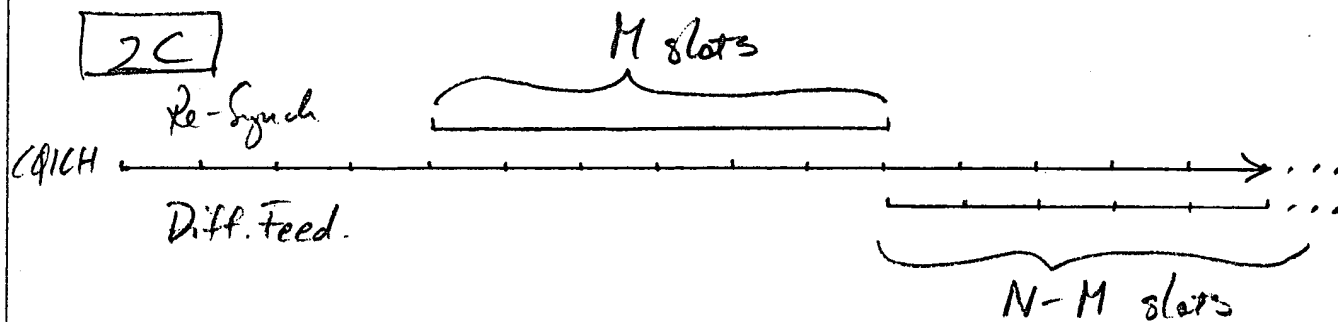
2A



2B



2C





206T20" B&B DWT

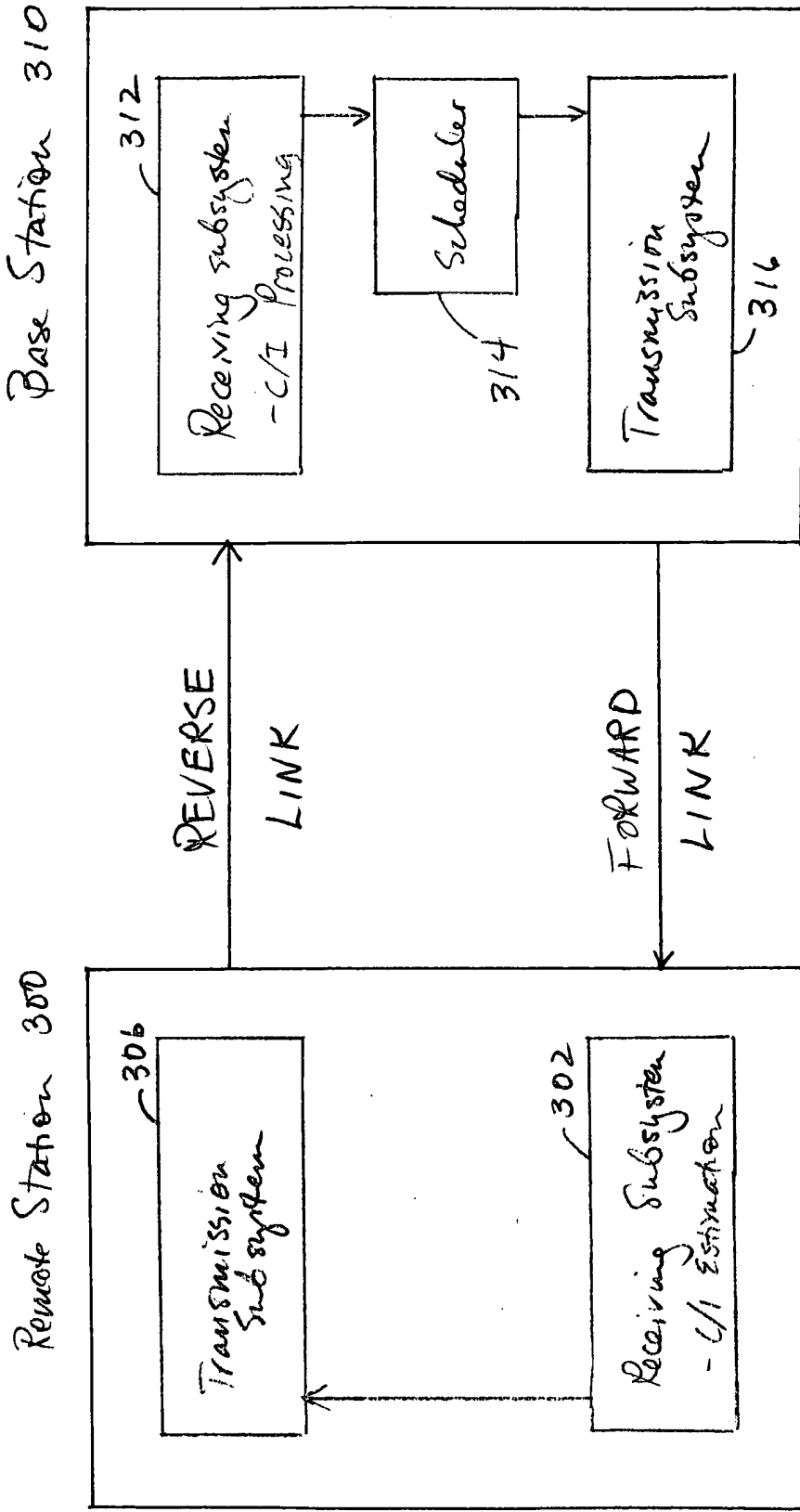
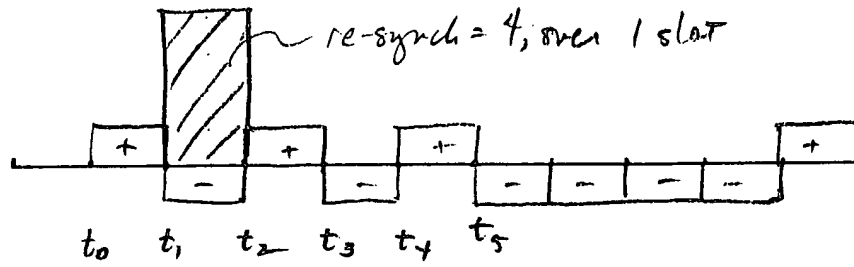
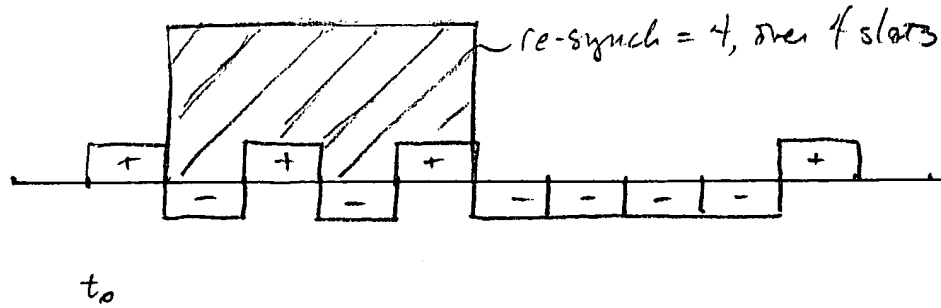


FIG. 3

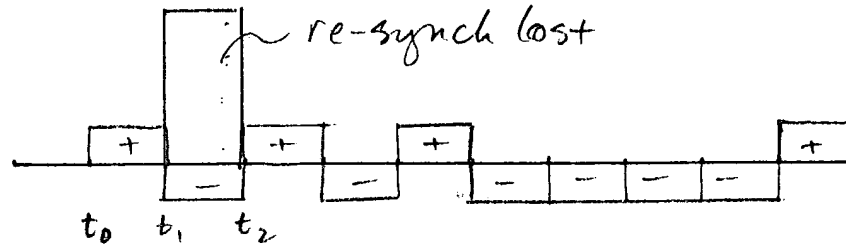
4A



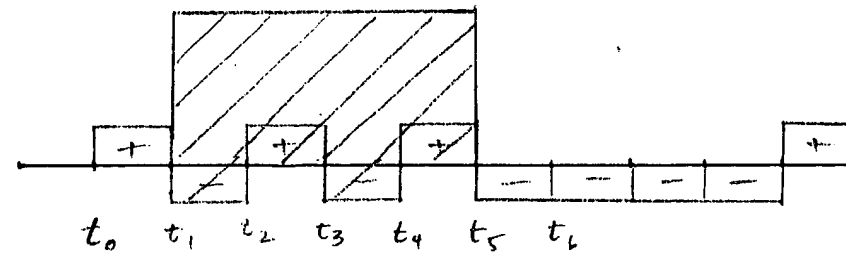
4B



4C



4D



At  $t_0$ , register contents = 10 dB  
+, -  $\Rightarrow$  incremental beats



206T20-B26C01

4A	4B	4C	4D
10	10	10	10
11	11	11	11
4	11	4	10
5	11	5	11
4	11	4	10
5	4	5	5 *
4	3	4	4

$t_0 - t_1$

$t_1 - t_2$

$t_2 - t_3$

$t_3 - t_4$

$t_4 - t_5$

$t_5 - t_6$

$t_6 - t_7$

\* Note that 1<sup>st</sup> parallel beat is discarded for "accumulate & add"

FIG. 4E

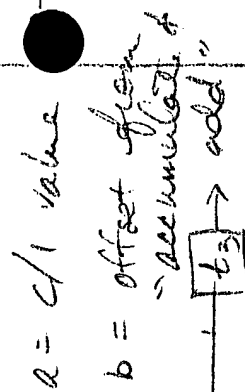


Fig. 5  
(not to scale)

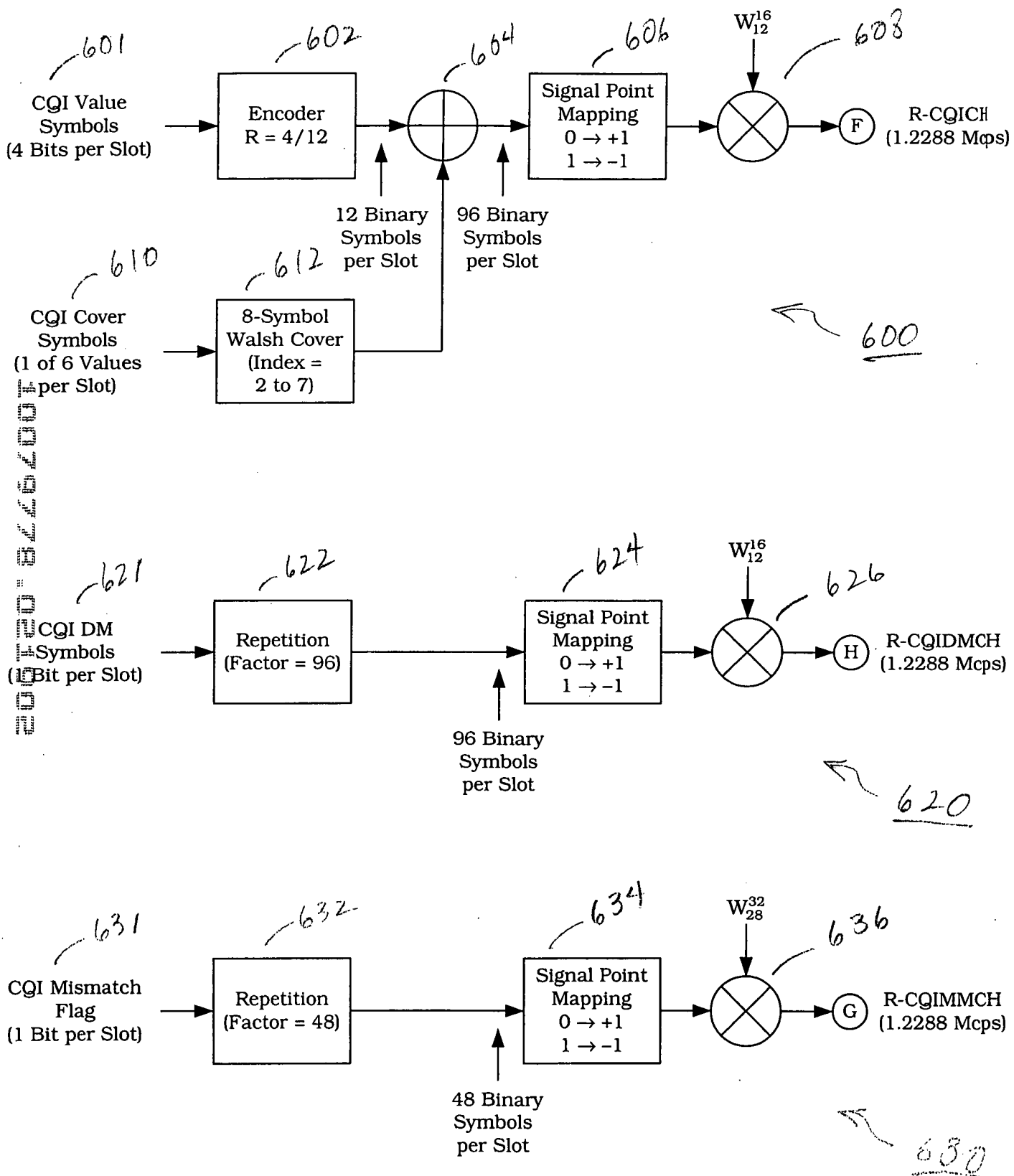


FIG. 6A

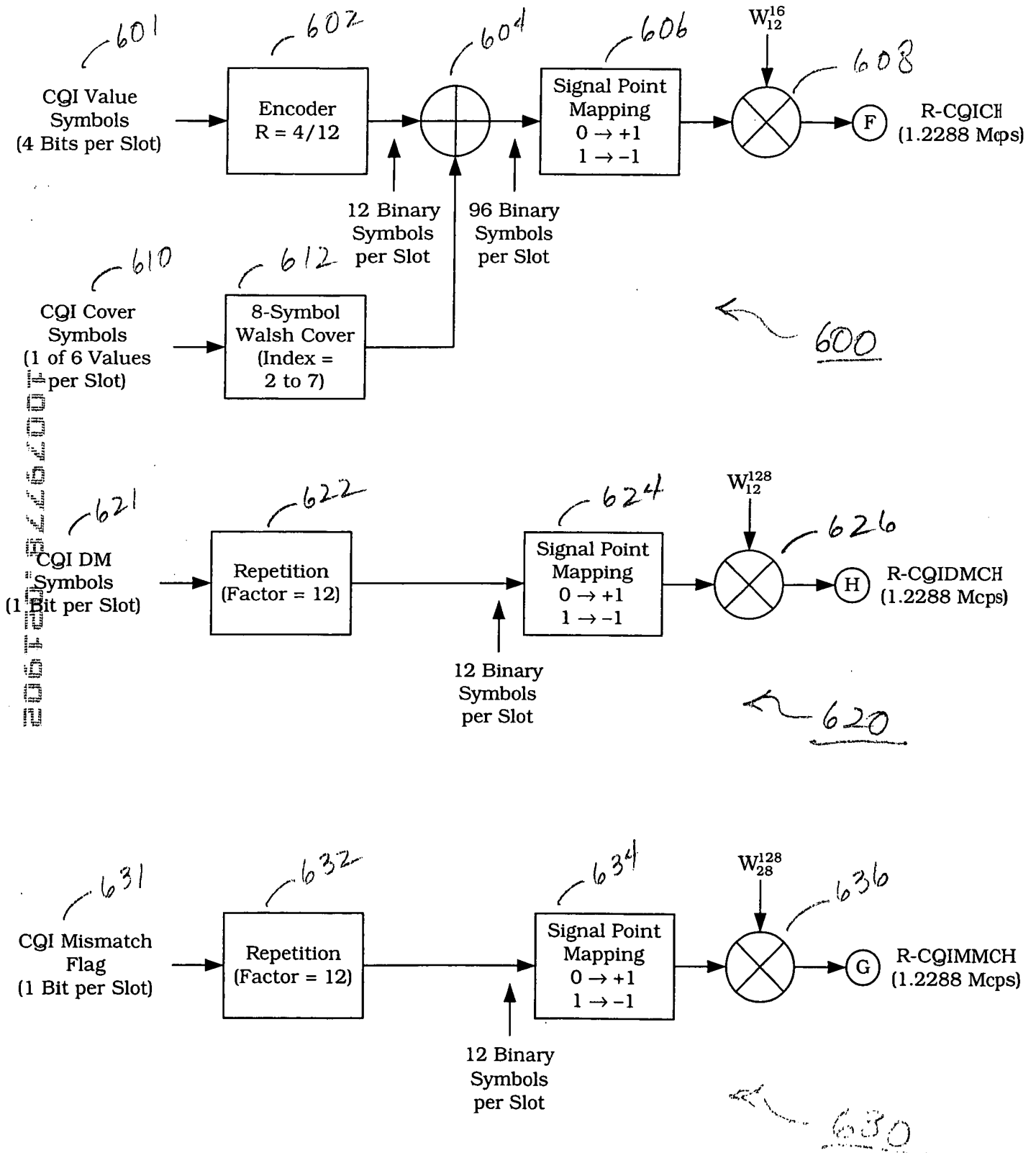
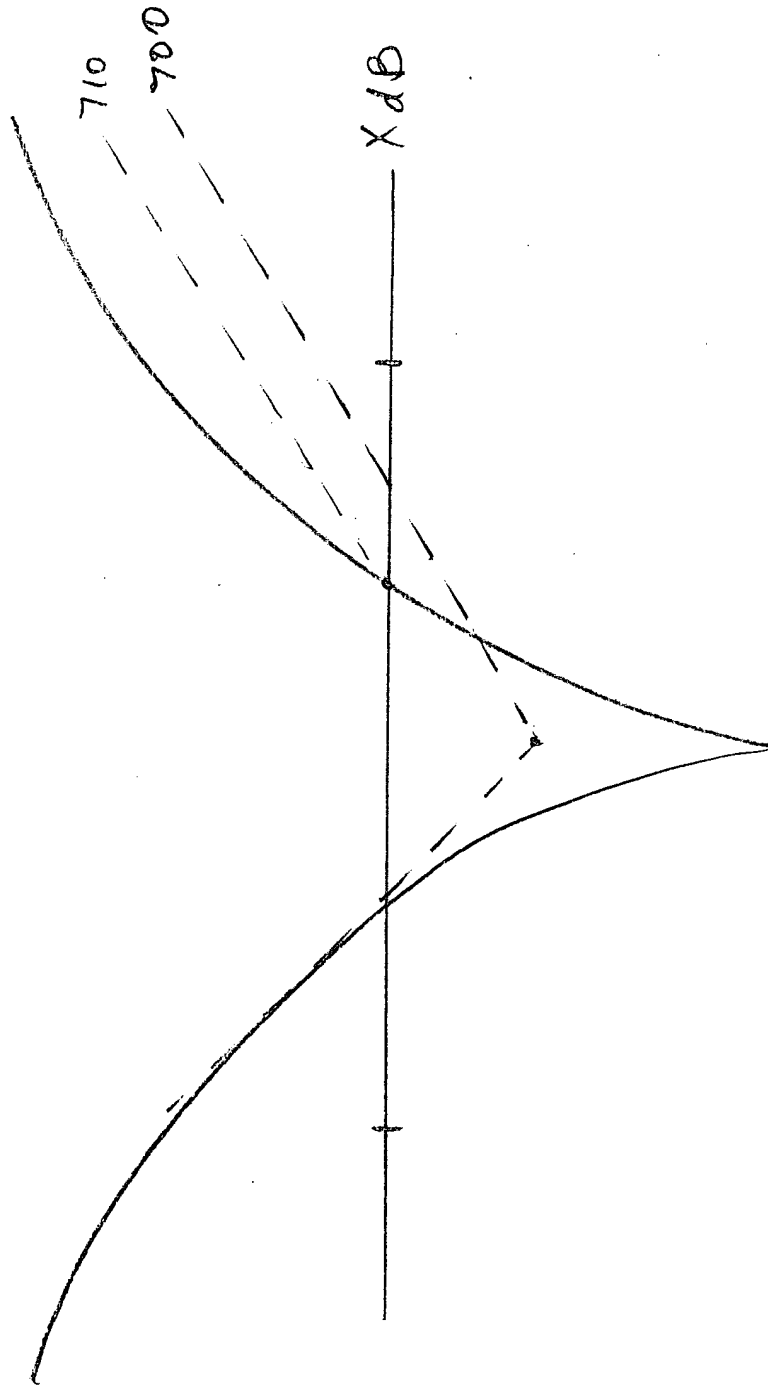


FIG. 6B





206120" 826084"



$t_{down}$   $t_{up}$   $t_{resynch}$   
Re-synch tx over M slots

FIG. 7  
(not to scale)